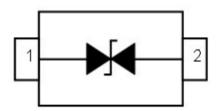
Low Capacitance Bi-directional ESD and Transient Voltage Protection

SD0525D32L SOD323



Pinout and Functional Block Diagram



Applications

- High Speed Line :USB1.0/2.0, VGA, DVI, SDI,
- Serial and Parallel Ports
- Projection TV
- Notebooks, Desktops, and Servers
- Cell Phone Handsets and Accessories
- Portable Instrumentation
- Peripherals

Order Information

Description

The SD0525D32L is designed to protect voltage sensitive components from ESD and transient voltage events. Excellent clamping capability, low leakage, and fast response time, make these parts ideal for ESD protection on designs where board space is at a premium. Because of its small size, it is suited for use in cellular phones, portable devices, digital cameras, power supplies and many other portable applications where board space comes at a premium. Also because of its low capacitance, it is suited for use in high frequency designs such as USB 2.0 high speed, VGA, DVI, SDI and other high speed line applications. This device has been specifically designed to protect sensitive components which are connected to data and transmission lines from overvoltage caused by ESD (electrostatic discharge), and EFT (electrical fast transients).

Features

- IEC61000-4-2 (ESD) ± 30 kV (Air), ± 30 kV (Contact)
- IEC61000-4-4 (EFT) 40 A (5 / 50 ns)
- Peak Power Dissipation: 400 W@8 / 20 μs
- Low Clamping Voltage
- Low Leakage Current
- High Temperature to Reflow Soldering Guaranteed: 260
 °C / 10 sec
- MSL1
- Flammability Rating: UL 94 V-0
- Halogen Free and RoHS Compliant

| Туре | Package | Marking Code | Delivery Form | Delivery Quantity |
|------------|---------|--------------|---------------|-------------------|
| SD0525D32L | SOD323 | 5/9 | 7" T&R | 3000 PCS |

Limiting Values

(T_A = 25 °C, unless otherwise specified)

| Symbol | Parameter | Conditions | | Max | Unit |
|------------------|---------------------------------|----------------------------------|-----|-----|------|
| | | IEC 61000-4-2; Contact Discharge | - | 30 | kV |
| V _{ESD} | Electrostatic Discharge Voltage | IEC 61000-4-2; Air Discharge | _ | 30 | kV |
| P _{PP} | Peak Pulse Power (8 / 20 μs) | - | - | 400 | W |
| T _A | Operating Temperature Range | - | -55 | 150 | °C |
| T _{stg} | Storage Temperature Range | - | -55 | 150 | °C |

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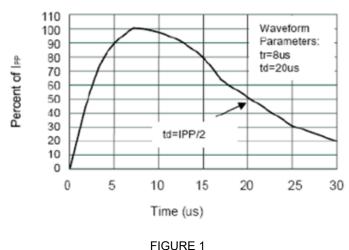
Electrical Characteristics

(T_A = 25 °C, unless otherwise specified)

| Symbol | Parameter | Conditions | Min | Тур. | Max | Unit |
|-----------------------|------------------------------|--|-----|------|-----|------|
| V_{RWM} | Max. Reverse Working Voltage | T _A = 25 °C | - | - | 5.0 | V |
| V_{BR} | Breakdown Voltage | I _R = 1 mA; T _A = 25 °C | 5.8 | - | 7.8 | V |
| I _R | Reverse Leakage Current | V _{RWM} = 5 V; T _A = 25 °C | - | - | 1.0 | μA |
| | | I _{PP} =1 A, t _P =8 / 20 μs | - | - | 9.8 | V |
| V _C | Clamping Voltage | I _{PP} =25 A, t _P =8 / 20 μs | - | 15 | 20 | V |
| С | Junction Capacitance | V _R = 0 V, f = 1 MHz | - | - | 60 | pF |

Performance Curve for Reference

(T_A=25 °C unless otherwise noted)





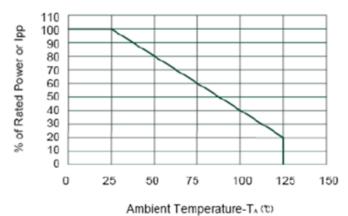


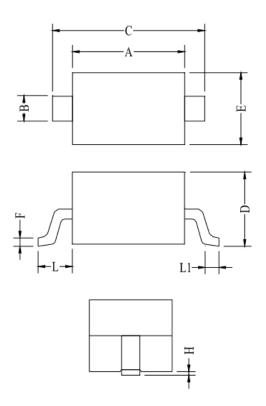
FIGURE 2
Power Derating Curve

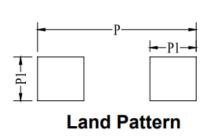


Low Capacitance Bi-directional ESD and Transient Voltage Protection

SD0525D32L SOD323

Package Dimensions - SOD323



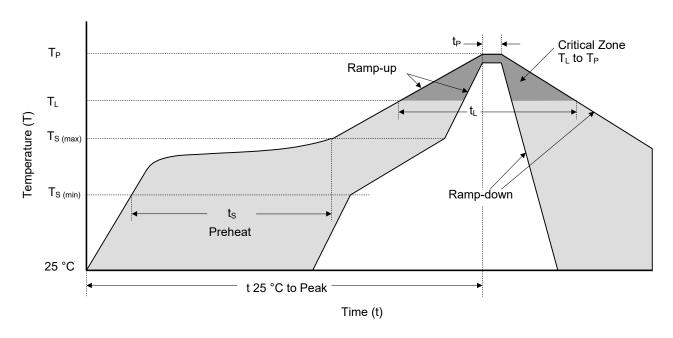


| Symbol | Millimeters | | Inches | | |
|--------|-------------|------|------------|-------|--|
| Symbol | Min. | Max. | Min. | Max. | |
| Α | 1.60 | 1.80 | 0.063 | 0.071 | |
| В | 0.25 | 0.40 | 0.010 | 0.016 | |
| С | 2.30 | 2.80 | 0.091 | 0.110 | |
| D | 0.80 | 1.10 | 0.031 | 0.043 | |
| E | 1.20 | 1.40 | 0.047 | 0.055 | |
| F | 0.08 | 0.18 | 0.003 | 0.007 | |
| L | 0.475 Ref. | | 0.019 Ref. | | |
| L1 | 0.25 | 0.40 | 0.010 | 0.016 | |
| Н | 0.00 | 0.14 | 0.000 | 0.006 | |
| Р | 3.00 | | 0.118 | | |
| P1 | 0.80 | | 0.031 | | |

Low Capacitance Bi-directional ESD and Transient Voltage Protection

SD0525D32L SOD323

Soldering Parameters



Reflowing Condition

| Reflow Solderi | Lead-Free Assembly | | | |
|--|--|--------------------|--|--|
| | Temperature Min (T _{S (min)}) | 150 °C | | |
| Pre-heat | Temperature Max (T _{S (max)}) | 200 °C | | |
| | Time (min to max) (t _s) | 60 ~ 120 seconds | | |
| Average Ramp Up Rate (L | Average Ramp Up Rate (Liquidus Temp (TL) to Peak | | | |
| T _S (max) to T _L | Ramp-up Rate | 3 °C / second max. | | |
| D 6 | Temperature (T _L) (Liquidus) | 217 °C | | |
| Reflow | Time (min to max) (t _L) | 60 ~ 150 seconds | | |
| Peak Temp | Peak Temperature (T _P) | | | |
| Time of within 5 °C of Act | Time of within 5 °C of Actual Peak Temperature (t _P) | | | |
| Ramp-do | 6 °C / second max. | | | |
| Time from 25 °C to | 8 Minutes max. | | | |
| Do Not | 260 °C | | | |

Low Capacitance Bi-directional ESD and Transient Voltage Protection

SD0525D32L SOD323



Usage

- 1. TVS must be operated in the specified ambient temp.
- 2. Do not clean the TVS with strong polar solvent such as ketone, esters, benzene and halogenated hydrocarbon, to avoid damaging the encapsulating layer.
- 3. Please do not apply severe vibration, shock or pressure to TVS, to avoid element cracking.

Replacement

- 1. If TVS is visually damaged, please replace it.
- 2. TVS is a non-repairable product. For safety sake, please use equivalent TVS for replacement.

Storage

- 1. Storage Temp. Range: (-55 to 150) °C.
- 2. Do not store the TVS at the high temp., high humidity or corrosive gas environment, to avoid influencing the solder- ability of the lead wires. The product shall be used up within 1 year after receiving the goods.

Environmental Conditions

- 1. TVS should not be exposed to the open air, nor direct sunshine.
- 2. TVS should avoid rain, water vapor or other condition of high temp. and high humidity.
- 3. TVS should avoid sand dust, salt mist, or other harmful gases.

Max. Typical Capacitance of TVS

1. The typical capacitance of TVS is listed in the specifications. Designers may refer to it when designing TVS in High frequency circuit.

Installation Mechanical Stress

- 1. Do not knock TVS when installing, to avoid mechanical damage.
- 2. Please do not apply severe vibration, shock or pressure to TVS, to avoid surface resin or element cracking.